

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

PUBLICATION NUMBER : 10098708  
PUBLICATION DATE : 14-04-98

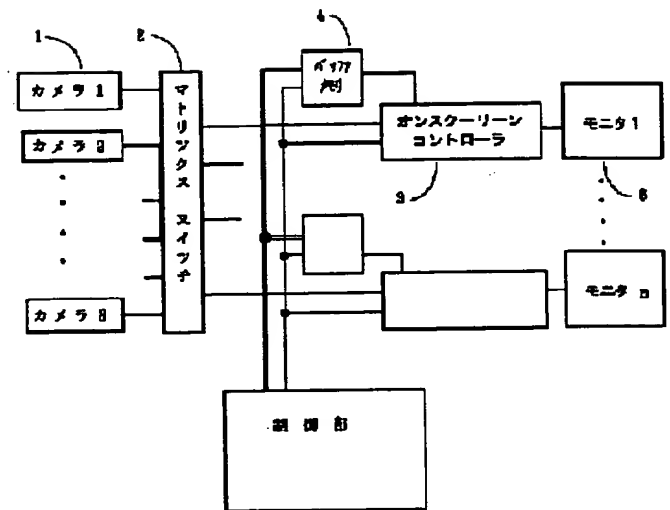
APPLICATION DATE : 20-09-96  
APPLICATION NUMBER : 08249866

APPLICANT : FUJITSU GENERAL LTD;

INVENTOR : NAKAYAMA TAKAMARO;

INT.CL. : H04N 7/18 G08B 13/196 H04N 5/00  
H04N 5/225 H04N 5/268 H04N 5/278  
H04N 5/445

TITLE : VIDEO MONITOR DEVICE



ABSTRACT : PROBLEM TO BE SOLVED: To make it possible to send out a video signal output generated by superposing a title signal on an output video signal by arranging a matrix switch and a specific number of on-screen controllers for its output together with a buffer memory which has a temporary storage function for a display data signal.

SOLUTION: A matrix switch 2 and a on-screen controllers 3 which are as many as or less than output video signals are united, and the on-screen controllers 3 are arranged on the output side of the matrix switch 2 together with the buffer memory 4. Then the buffer memory 4 stores temporarily the display data sent in series from a control part and when video signals are switched, the data are transferred to the respective on-screen controllers 3. Consequently, the video input signals are optionally switched at the same time and the video signal output generated by superposing a title signal of characters, figures, etc., on output signals can be sent out.

COPYRIGHT: (C)1998,JPO

BEST AVAILABLE COPY